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JL  
Bouchard 3-7-4-4-7-2

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

### Patent Application

Applicant(s): G.A. Bouchard et al.

Case: 3-7-4-4-7-2

Serial No.: 10/029,679

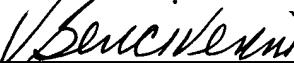
Filing Date: December 21, 2001

Group: 2152

Examiner: To Be Assigned

Title: Methods and Apparatus for Using Multiple Reassembly  
Memories for Performing Multiple Functions

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature:  Date: June 10, 2005

### STATUS REQUEST

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

To date, we have received no communication or official action from the Examiner in charge of the above-identified patent application. Please let us know the status of this case.

Respectfully submitted,



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Date: June 10, 2005